



LORDS INSTITUTE OF ENGINEERING & TECHNOLOGY

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Department of Information Technology

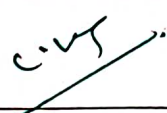
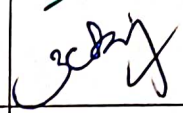

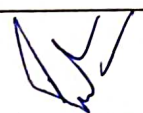
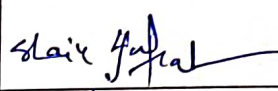

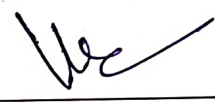
Date: 21-08-2021

Minutes of the of First Board of Studies Meeting

The first Board of Studies Meeting of the Department of Information Technology was held on 21-08-2021 at 11:00 AM in Board Room, Lords Institute of Engineering and Technology, Hyderabad.

Members Present:

Sl. No.	Name of the BoS Member	External/ Internal	Signature
1.	Dr. K.Nagi Reddy Professor & HOD, Dept. of Information Technology, Lords Institute of Engineering and Technology Hyderabad	Chairperson, BoS	
2.	Dr. P V Sudha Professor and Chairperson, Board of Studies Department of CSE University College of Engineering, Osmania University, Hyderabad.	External Member (OU Nominee)	
3.	Dr.A.Nagesh Professor of CSE, MGIT, Hyderabad	External Member (Subject Expert)	
4.	Dr. V. Venkata Krishna Professor in IT, Vasavi College of Engineering (A), Hyderabad.	External Member (Subject Expert)	
5.	Mr. G.Sravan Kumar Technical Architect ACCELERIZE 360, Hyderabad	External Member (Industry Expert)	
6.	Mr. S.Rafeeuddin Package Solution Consultant, IBM, Hyderabad.	External Member (Alumni)	

7.	Dr.C.Venkata Narasimhulu Principal, LIET	Permanent Invitee	
8.	Dr. Mohammed Jabirullah HOD,ECE Dept., LIET	Special Invitee	
9.	Dr. J. Sasi Kiran Professor, CSE Dept., LIET	Internal Member	
10.	Dr. R. Hafeez Basha Professor, CSE Dept., LIET	Internal Member	
11.	Mr. Shaik Imam Saheb Associate Professor, CSE Dept., LIET	Internal Member	
12.	Mrs. M.Neelima Associate Professor, IT Dept., LIET	Internal Member	
13.	Mr.D.Venkatesh Assistant Professor IT Dept., LIET	Internal Member	

Agenda:

1. Discussion and Finalization of 4 years B.E. Information Technology Programme Course Structure
2. Discussion and Finalization of Professional Electives & Open Electives
3. Discussion and Finalization of Skill Development courses and Internship Programmes
4. Suggestions on First year Syllabus designed by other Departments

Dr. K.Nagi Reddy, HOD – IT & Chairperson-BoS, LIET (A) welcomed all the members of the Board of Studies and other faculty members. On the request of Board of Studies Chairperson, the members introduced themselves. This was followed by the discussion on agenda points mentioned above.

1. BoS Chairperson presented the salient features, curriculum analysis, and I, II, III & IV Year B.E. Course Structure to the members of the BoS. After presenting the UG course structure the Chairperson welcomed suggestions and comments from all the BoS members.
2. The BoS members appreciated the BOS chairperson and faculty members for their efforts in designing the course structure and gave suggestions on UG course structure.

The following deliberations and resolutions were made:

Item No. 1: Discussion and Finalization of 4 years B.E. Information Technology Programme Course Structure

The B.E. – Information Technology Course Structure for I to VIII semesters under the Regulations 2021-22 was presented by the chairperson, BoS. The BoS members have noted, discussed and approved the course structure with minor suggestions as mentioned below:

Semester-I:

Deliberations:

- ❖ Mandatory course (Indian Constitution) should be completed in I Semester for regular students and same mandatory course should be completed by lateral entry students in III Semester.

Resolutions:

- ❖ Members noted and approved.

Semester-II:

Deliberations:

- ❖ Two mandatory courses should be completed in II semester (1.Environmental Science and 2.Essence of Indian Traditional Knowledge) for regular students and same mandatory courses should be completed by Lateral Entry students in IV Semester.

Resolutions:

- ❖ Members noted and approved

Semester-III:

Deliberations:

BoS Chairperson has explained the reasons for Merging Analog Electronics with Digital Electronics into a single subject namely "Analog and Digital Electronics".

Members suggested to replace Analog and Digital Electronics lab with MATLAB Lab as MATLAB Concepts are required to implement Mini and Major Projects

Members also suggested to change the name of the IT Workshop Lab to Python Programming Lab as the same title is repeated in I-Semester also

Resolutions

- ❖ Analog and Digital Electronics lab is to be replaced with MATLAB Lab
- ❖ IT Workshop lab is to be renamed as Python Programming Lab
- ❖ Members noted and approved

Semester-IV:

Deliberations:

As Analog and Digital Electronics Lab is deleted from the Curriculum, Members suggested to include some Programs of Digital Electronics in Microprocessor Lab Syllabus

Resolutions

- ❖ Some Digital Electronics Programs should be included in Microprocessor lab while framing the Syllabus
- ❖ Members noted and approved

Semester-V:

- ❖ Members noted and approved

Semester-VI:

- ❖ Members noted and approved

Semester-VII:

- ❖ Members noted and approved

Semester-VIII:

- ❖ Members noted and approved

Item No.2: Discussion and Finalization of Professional Electives & Open Electives

- ❖ Members noted and approved

Item No. 3: Discussion and finalization of Skill Development Courses and Internship Programmes

Deliberations

- ❖ All the Members appreciated for including "Aptitude and Reasoning" Skill Development Course in the 5th Semester.

Resolutions

- ❖ Members noted and approved

Item No. 4: Suggestions on First year Syllabus designed by other Departments

- ❖ Members Noted and approved the I Year Proposed Syllabus

The approved four year B.E. Information Technology Course Structure is attached as Annexure-I.

BoS members have approved the modified course structure for Academic Year 2021-22. It is further resolved that the course structure may be modified in subsequent BoS Meetings.

BoS Chairperson is authorized to present the approved course structure to the Academic Council for further suggestions.

BoS Chairperson Thanked all the BOS members for spending quality time in reviewing the Course Structure and for providing valuable inputs.



Dr. K.Nagi Reddy

BoS Chairperson & Head,

Department of Information Technology,

Lords Institute of Engineering and Technology,

Hyderabad.

2. P.V. Guleri 3. [Signature] 4. [Signature] 5. [Signature] 6. [Signature] 7. [Signature] 8. [Signature] 9. [Signature] 10. [Signature] 11. [Signature] 12. [Signature] 13. [Signature]