



# LORDS INSTITUTE OF ENGINEERING & TECHNOLOGY (Autonomous)

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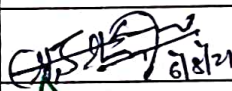

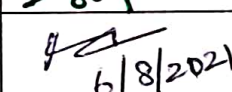
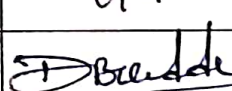
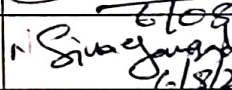
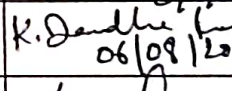
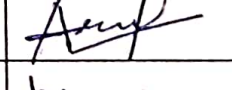
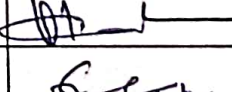
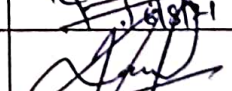
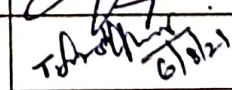
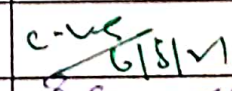
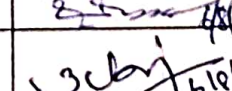
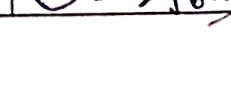

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Date: 06-08-2021

## MINUTES OF THE MEETING OF FIRST BOARD OF STUDIES

The first Board of Studies Meeting of the Department of Electrical and Electronics Engineering was held on 06-08-2021 at 2:00 PM in Board Room, Lords Institute of Engineering and Technology, Hyderabad.

### Members Present:

Sl No.	Name of the BoS Member	External/ Internal	Signature
1	Dr. Ch. Santhan Kumar Associate Professor & Head, EEE, LIET	Chairman	
2	Dr. P. Sathish Kumar Professor, Dept. of EE, College of Engineering, OU, Hyderabad	External Member (OU Nominee)	
3	Dr. J. Sridevi Professor & Head, Dept. of EEE, GRIET, Hyderabad	External Member (Academic Expert)	
4	Dr. D. Bala Gangi Reddy Professor, Dept. of EEE, VJIT, Hyderabad	External Member (Academic Expert)	
5	Mr. N. Sivagangadhar Manager- Schneider Electric, Hyderabad	External Member (Industry Expert)	
6	Mr. K. Dasharad Technical Lead, Value Labs, Hyderabad	External Member (Alumnus)	
7	Dr. A. Arul Prakash Professor, EEE, LIET	Internal Member	
8	Mr. R. Venkata Krishna Assistant Professor, EEE, LIET	Internal Member	
9	Mr. G. Karunakar Reddy Assistant Professor, EEE, LIET	Internal Member	
10	Mr. V. Karthik Assistant Professor, EEE, LIET	Internal Member	
11	Mr. T. Santhosh Kumar Assistant Professor, EEE, LIET	Internal Member	
12	Dr. C. V. Narasimhulu, Professor, ECE, Principal, LIET	Permanent Invitee	
13	Dr. J. Sasi Kiran, Dean First Year, LIET	Special Invitee	
14	Dr. Mohammad Jabirullah, Autonomous Coordinator, LIET	Special Invitee	

## **AGENDA:**

1. Introduction of BoS Members to the faculty
2. Introduction of the faculty members to the BoS members
3. Brief introduction of the department
4. Discussion on course structure of all B.E. Semesters and finalizing the B.E. I Year Syllabi related to EEE subjects
5. Discussion on course structure of all M.E. (Power Electronics) Semesters and finalizing the M.E. I Year Syllabi related to EEE subjects

Dr. Ch. Santhan Kumar, BoS Chairman and HOD (EEE), welcomed all the members of the Board of Studies, Principal, and other faculty members to the meeting. This was followed by the discussion on the agenda points mentioned above:

### **1. Introduction of BoS Members to the faculty:**

BoS Chairman introduced the BoS members to the respected Principal, LIET, and other faculty members of the department.

### **2. Introduction of the faculty members to the BoS members:**

BoS Chairman introduced the department faculty members to Honorable BoS members

### **3. Brief introduction of the department:**

BoS Chairman has presented the department profile to the BoS Members.

The following deliberations were made as per the agenda and resolutions were listed as below:

### **4. Discussion on course structure of all B.E. Semesters and finalizing the B.E. I Year Syllabi related to EEE subjects:**

BoS Chairman presented the salient features, curriculum analysis, course structure of the B.E. Programme of all semesters, and I year EEE courses syllabi to the members of the BoS. After presenting the UG course structure and syllabi, the Chairman welcomed suggestions and comments from all BoS members.

The BoS members appreciated the BOS chairman and faculty members for their efforts in designing the course structure and suggested the following points in the UG course structure and syllabus.

### **Semester-I:**

BoS Members suggested the following points:

- Include the characteristics, types, and applications topics in synchronous machines in the IV unit of the BEE subject.
- Add two more experiments in the BEE laboratory preferably
  - i. Experiment on Single-Phase Induction Machines
  - ii. OC and SC characteristics of the Alternator

### **Semester-II:**

- The members noted and approved



**Semester-III:**

- The members noted and approved

**Semester-IV:**

- The members noted and approved

**Semester-V:**

- Include Switch Gear Protection in Power System-II course
- More care should be taken in designing the syllabus for the Power System Lab
- The members noted and approved

**Semester-VI:**

- The members noted and approved

**Semester-VII:**

- The members noted and approved

**Semester-VIII:**

- Students must be given the choice of selecting the subjects for comprehensive viva
- The members noted and approved

The following are the common points suggested by the BoS members for UG course structure and syllabus

- Syllabus preparation to be taken utmost care
- Industrial visits are to be planned frequently for practical knowledge
- Introduce electrical software and provide some hands-on training to students
- Plan more workshops and guest lectures by experts from the core and IT sectors to get the awareness of recent developments
- Proof of concept plays a vital role in the industry, to be implemented in projects and seminar reports

**Note:**

1. UG EEE all semesters Course Structure is attached as Annexure – I
2. UG I Year BEE Syllabus is attached as Annexure – II

**5. Discussion on course structure of all M.E. (Power Electronics) Semesters and finalizing the M.E. I Year Syllabi related to EEE subjects:**

Dr. Ch. Santhan Kumar, BoS Chairman presented the course structure of the M.E. (Power Electronics) Programme of all semesters and I year EEE courses syllabi to the members of the BoS. After presenting the PG course structure and syllabi, the BoS Chairman welcomed suggestions and comments from all BoS members.

BoS Members suggested the following points

**Semester-I:**

- Professional Electives to be rearranged as per the M.E. (Power Electronics) program requirement
- The experiments in Power Converters Lab-1 should be more advanced compared to UG experiments

**Semester-II:**

- The experiments in Power Converters Lab-II should be advanced compared to Power Converters Lab-1

**Semester-III:**

- The members noted and approved

**Semester-IV:**

- The members noted and approved

**Note:**

1. PG(Power Electronics) course structure is attached as Annexure – III
2. PG(Power Electronics) I Year Syllabi is attached as Annexure – IV

BOS members have approved the modified course structure and syllabus for the Academic Year 2021-22. It is further resolved that the course structure and syllabus maybe modified in subsequent BOS meetings.

BOS Chairman is authorized to present the approved course structure to Academic Council for further suggestions.

BoS Chairman conveyed a vote of thanks to all the BOS members for spending their valuable time in reviewing the course structures & syllabi, all the Deans and HODs of other departments, department faculty members for the support in designing course structures of UG and PG, the Principal for continuous guidance and the Management for constant encouragement and support.

 6/12/21  
Dr. Ch. Santhan Kumar

BoS Chairman & Head  
Department of Electrical and Electronics Engineering  
Lords Institute of Engineering and Technology (Autonomous)  
Hyderabad